EXHIBIT B

Exhibit B: Claim Chart for U.S. Pat. No. 6,346,795

Based on:

U.S. Pat. No. 5,959,437 to Hamaguchi ("the '437 patent") (Filed Feb. 9, 1998; Priority Date Feb. 10, 1997; Issued Sept. 28, 1999)

Claim Element [[pre]] 1	Claim Language — U.S. Patent No. 6,346,795 A discharge control circuit for controlling discharge of a battery including at least	Exemplary Disclosures from the '437 patent To the extent the preamble is limiting, the '437 patent discloses "a discharge control circuit for controlling discharge of a battery including at least one cell," at least under Celebration IP's interpretation of that term in its Complaint
	for controlling discharge of a battery including at least one cell comprising:	circuit for controlling discharge of a battery in Celebration IP's interpretation of that term in See, e.g.:
		"To obtain a required delay time at each control using one capacitor in common, a charge and discharge control circuit includes an overcharge detecting circuit, an overdischarging circuit , and an overcurrent detecting circuit and carries out charge and discharge control of a secondary cell by ON/OFF control." ('437, abstract) (emphasis added).
		" the charge and discharge control circuit of the present invention controls a charge and discharge of a Secondary cell connected to an external power source terminal through a Switch circuit by ON/OFF controlling said switch circuit and further includes an overdischarge detecting circuit " ('437, Col. 1, lines 45-50) (emphasis added).
1[a]	a discharge control switch connected to the battery for cutting off a discharge current of the battery in response to a discharge stop signal; and	The '437 patent discloses "a discharge control switch connected to the battery for cutting off a discharge current of the battery in response to a discharge stop signal at least under Celebration IP's interpretation of that term in its Complaint. See, e.g.:

	Claim Element
	Claim Language — U.S. Patent No. 6,346,795
**HG. 1) (emphasis added) **FIG. 1 is a circuit block diagram of a charge and discharge control circuit of the present invention. A negative pole of a secondary cell 101 is connected to an external power source terminal -V0 through a switch circuit 103 comprises two N-channel FETs, in the embodiment shown in the figure, an FET 113 for overdischarge control is formed at the external power source terminal -V0." ('437, Col. 2, lines 36-44) (emphasis added)	Exemplary Disclosures from the '437 patent

a Q	Claim Claim Language — U. Element Patent No. 6,346,795
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To the extent that the '437 patent does not expressly disclose this claim element, the '437 patent, alone or in combination with other prior art, renders the claimed subject matter obvious. For example, as shown in Exhibit A, U.S. Patent No. 5,397,974 to Tamai, et al. ("the '974 patent") discloses this claim element, at least as Celebration IP has interpreted it in its Complaint. A person of ordinary skill in the art would have been motivated to combine the '437 patent and the '974 patent at least because the patents both relate to battery discharge control and purport to solve similar problems. The '437 patent discloses "a control circuit connected to the battery and the discharge control switch for generating the discharge stop signal that deactivates the discharge control switch when a voltage of at least one cell reaches a lower limit," at least under Celebration IP's interpretation of that term in its Complaint. See, e.g.,	Exemplary Disclosures from the '437 patent

Claim Element	Claim Language — U.S. Patent No. 6,346,795	Exemplary Disclosures from the '437 patent
		unit 130, and an output control logic circuit 124." ('437, Col. 2, lines 44-58) (emphasis added)
		"The overdischarge detecting comparator 118 has a construction detecting an overdischarge state comparing reference voltage Vr of the reference voltage circuit 116 with the divided output reflecting the terminal voltage of the secondary cell 101 occurring at both terminals of the resistor R5 of the voltage divider circuit 121." ('437, Col. 3, lines 22-27).
		"An inverter circuit 129 is formed at output side of the overcharge detecting comparator 118, and output B changing low level to high level is output from the inverter circuit 129 when output of overdischarge detecting comparator 118 changes high level to leave the level to have been at the overdischarge state is detected.
		Output B is input to the delay unit 130, and when output B turns high level from low level for example, output B" of the delay unit 130 can change low level to high level with the predetermined delay time." ('437, Col. 3, lines 33-41) (emphasis added).
		To the extent that the '437 patent does not expressly disclose this claim element, the '437 patent, alone or in combination with other prior art, renders the claimed subject matter obvious. For example, as shown in Exhibit A, the '974 patent discloses this claim element at least as Celebration IP has interpreted it in its Complaint. A person
		of ordinary skill in the art would have been motivated to combine the '437 patent and the '974 patent at least because the patents both relate to battery discharge control and purport to solve similar problems.
1[c]	wherein the control circuit includes a switch holding circuit for	The '437 patent discloses a control circuit that "includes a switch holding circuit for continuously supplying the discharge stop signal to the discharge control switch for a predetermined time after the discharge stop signal is generated," at least under
	supplying the	
	discharge stop signal to the discharge	See, e.g.,

		Claim Element
	control switch for a predetermined time after the discharge stop signal is generated.	Claim Language — U.S. Patent No. 6.346.795
"An inverter circuit 129 is formed at output side of the overcharge detecting comparator 118, and output B changing low level to high level is output from the inverter circuit 129 when output of overdischarge detecting comparator 118 changes high level to low level, namely when the overdischarge state is detected. Output B is input to the delay unit 130, and when output B turns high level from low level for example, output B" of the delay unit 130 can change low level to high level with the predetermined delay time." ('437, Col. 3, lines 33-41) (emphasis added).	FIG.1 FI	Exemplary Disclosures from the '437 patent